

IN THE CLAIMS

1-21. (Canceled)

22. (Currently amended) A computing system, said computing system comprising:

an input device and a computing device, said input device including a memory, a controller having a first CPU and an electronic pen input device operative to emit a first signal having a first characteristic and a second signal having a second characteristic, said computing device including a second CPU;

a detector for detecting said characteristics of the emitted signals; and

a-wherein said controller is interfaced with said input device detector so that said first CPU selectively interprets the emitted signals as one of information to be stored by said input device system and information to be forwarded to said computing device and that routes (a) to said memory input device the information to be stored therein and (b) to the computing device the information to be forwarded thereto for processing by said second CPU.

23. (Currently amended) The electronic pen input device computing system of claim 22 comprising a switch wherein the characteristic of said emitted signal is determined by operation of said switch.

24. (Currently amended) The computing system electronic pen input device of claim 22, wherein said electronic pen input device comprises first and second further comprising two tips that emit said first and second signals,

respectively, and wherein the characteristic which of said emitted first and second signals is emitted depends on which one of said two first and second tips is selected by a user.

25. (Canceled)

26. (Currently amended) The computing system of claim 25 wherein said local memory comprises Flash RAM type memory.

27. (New) The computing system of claim 22, wherein said second CPU of said computing device responds to said information forwarded thereto based on detection of said second signal to control a display of said computing device.

28. (New) The computing system of claim 22, wherein said second CPU of said computing device responds to said information forwarded thereto based on detection of said second signal to control a function of said computing device.